

sets the buffer address in a register of said adapter, and said adapter extracts the data from the buffer address set in the register, and transmits the extracted data to the initiator.

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5. The device driver apparatus according to claim 1, wherein
said adapter notifies said driver of command upon receipt of the command from the initiator, said driver notifies the PIO process of the notified command, the PIO process prepares a buffer, and returns a buffer address to said driver, said driver sets the buffer address in a register of said adapter, said adapter stores data that is requested of the initiator and received at the buffer address set in the register, and notifies said driver that the data has been stored, said driver notifies the PIO process that the data has been received, the PIO process extracts the data from the buffer, and returns status being a reply to said driver, said driver sets the status in a register of said adapter, and said adapter returns the status to the initiator.

6. The device driver apparatus according to claim 1, wherein
said driver is configured by a low-order driver for said adapter, a high-order driver for the PIO process, and a medium-order driver transmitting/receiving a signal between the low-order driver and the high-order driver.

7. The device driver apparatus according to claim 1, wherein
the PIO process notifies said adapter or said driver of an error, and said adapter or said driver makes the notified error occur.

8. The device driver apparatus according to claim 1, wherein
the PIO process simulates an actual I/O device by transmitting/receiving status or data of a specified I/O device.

9. The device driver apparatus according to claim 1, wherein
the PIO process simulates an error test of an actual I/O device by making a specified error occur when status or data of a specified I/O device is transmitted or received.